TD101X Series

LSOP4, DC Input, Photo Transistor Coupler

De$cri%tion

Feat&re$

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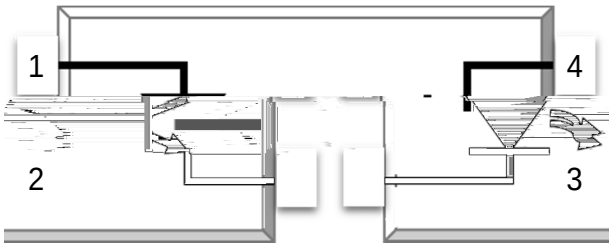
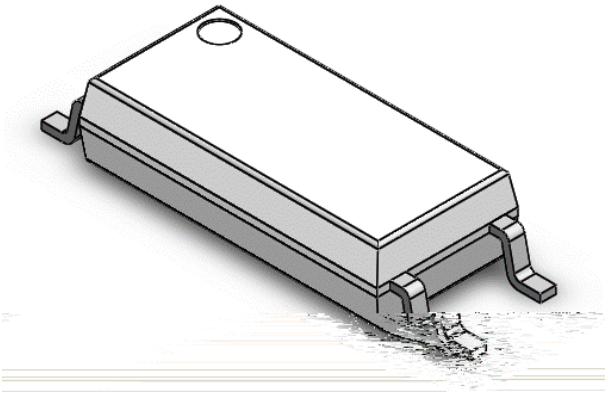
SCHEMATIC



PIN DEFINITION

1. Anode
2. Cathode
3. Emitter
4. Collector

PAC A!E O"T#INE



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Note 1. 100µs pulse, 100 ! "#e$uenc%

Note 2. A& 'o# 1 ( )nute, R. . * +0 , -0 .

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Fi..3 For/ard C&rrent **0$**. For/ard -olta.e	Fi..4 Collector Dar**2** C&rrent **0$**. Am**1**ient Tem%erat&re
Fi..+ Collector C&rrent **0$**. Collector3emitter -olta.e	Fi..**4** Collector C&rrent **0$**. Collector3emitt**2.81416(**t**2.81416(**t**2.1**

CHARACTERISTIC CURVES

Fig.5 Normalized Current Transfer Ratio vs. Forward Current

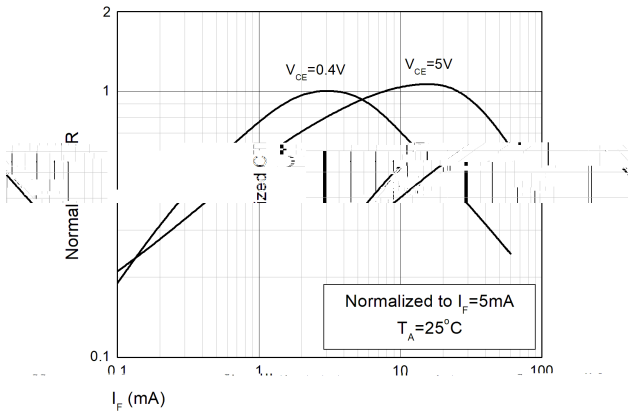


Fig.8 Normalized Current Transfer Ratio vs. Ambient Temperature

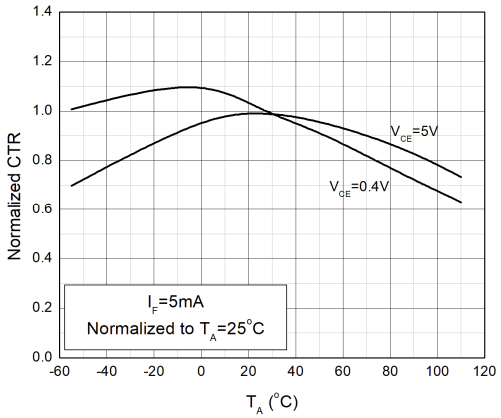


Fig.9 Collector-emitter Saturation Voltage vs. Ambient Temperature

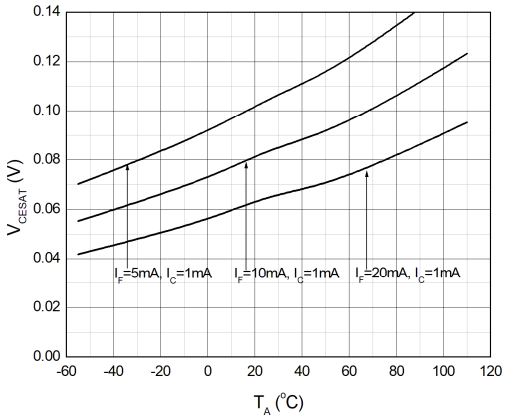


Fig.11 Switching Time vs. Load Resistance

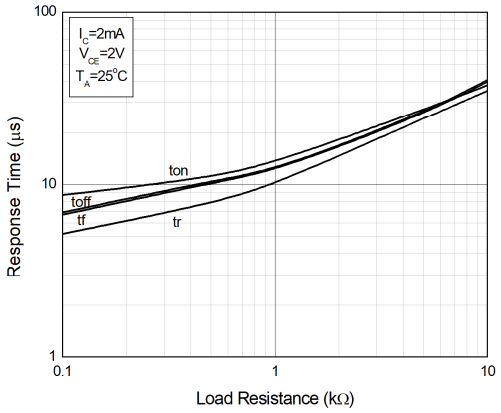
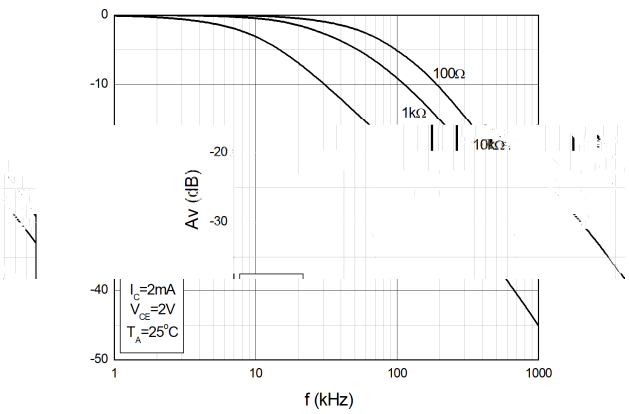


Fig.11 Frequency Response



TEST CIRCUITS

Fig.12 Test Circuit for Response Time

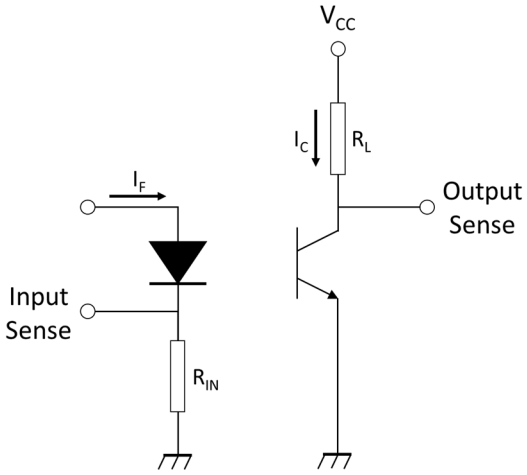


Fig.13 Circuit for Response Time

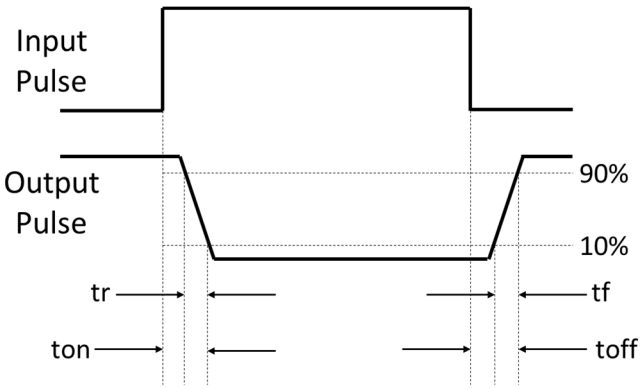
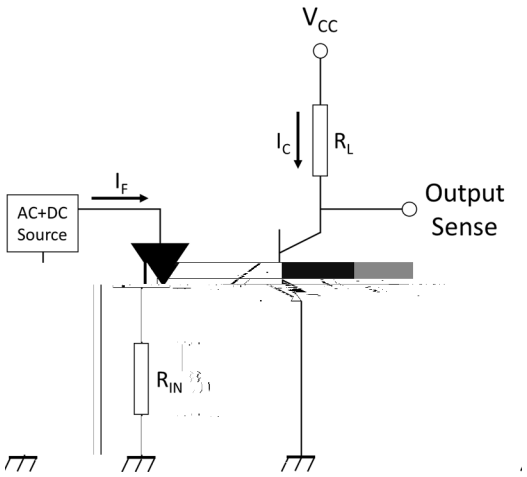
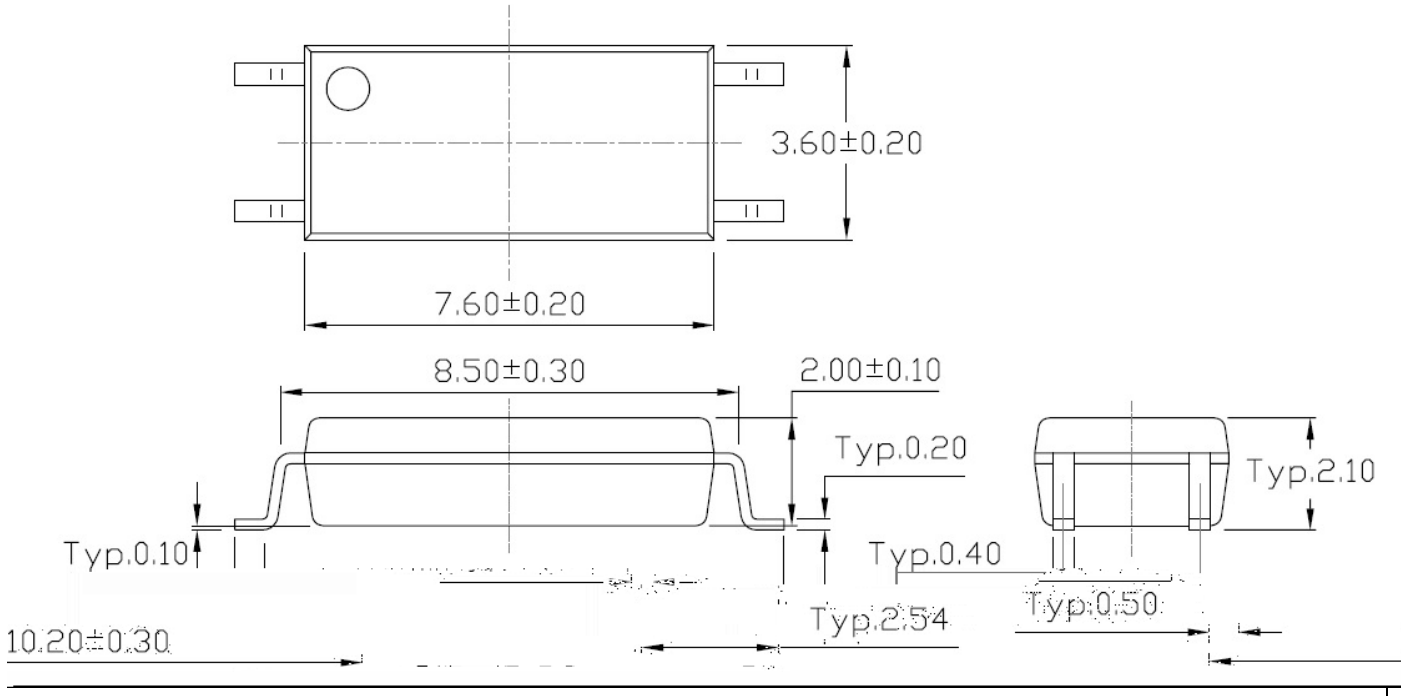


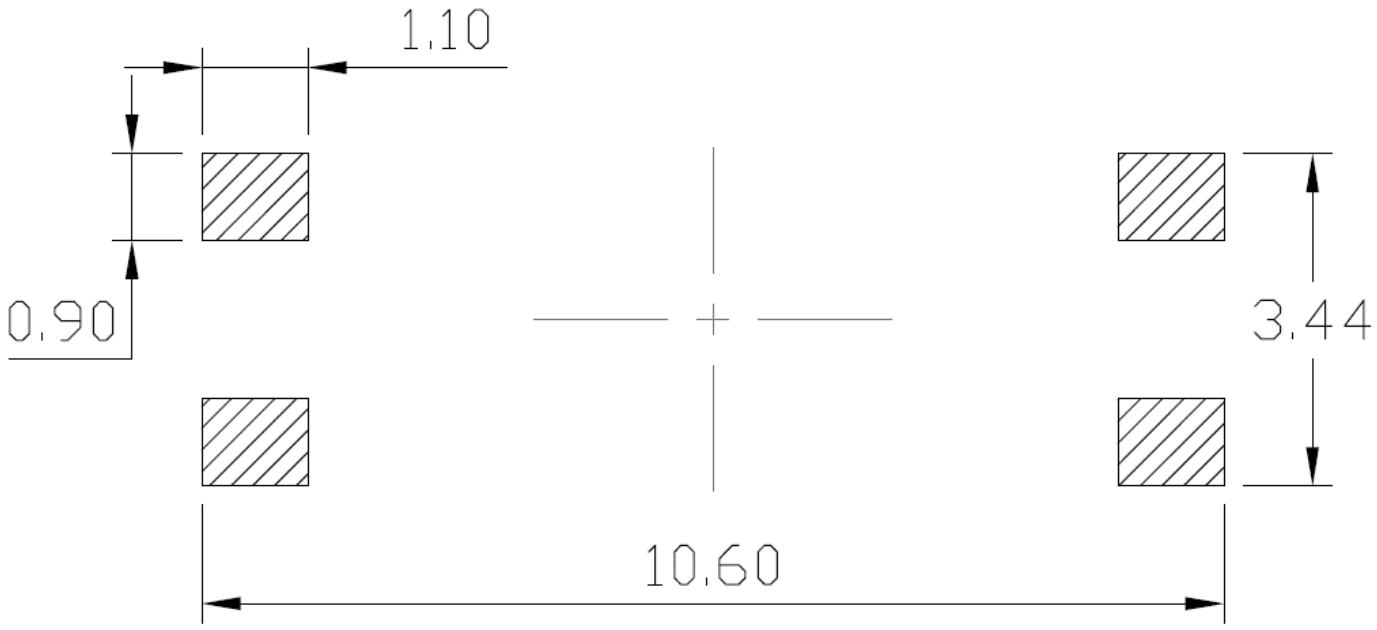
Fig.14 Test Circuit for Frequency Response



PACKAGE DIMENSIONS (Dimensions in mm unless other wise stated)

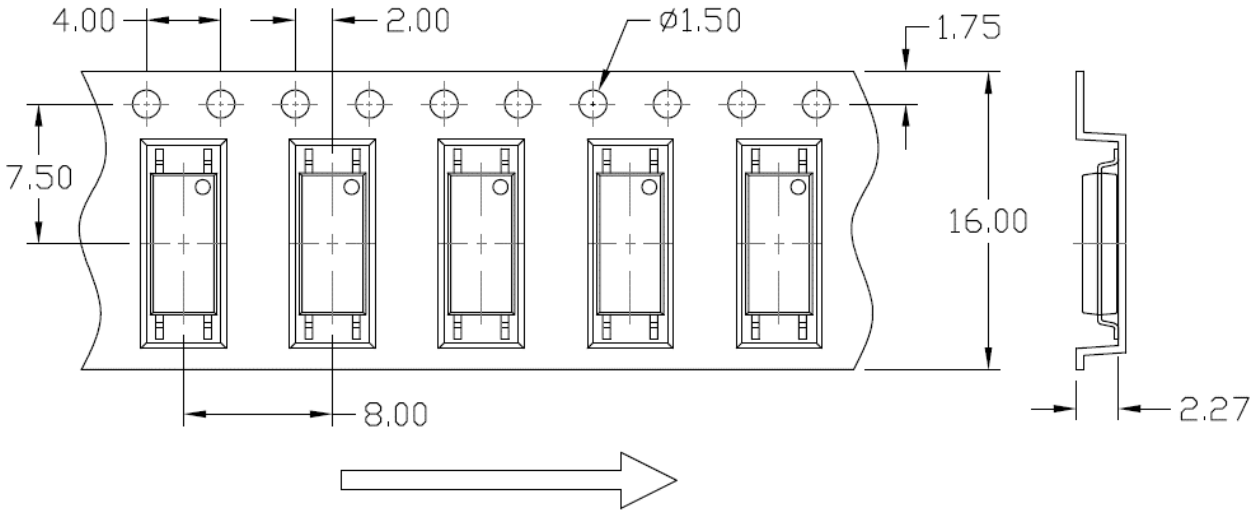


RECOMMENDED SOLDER PAD (Dimensions in mm unless other wise stated)

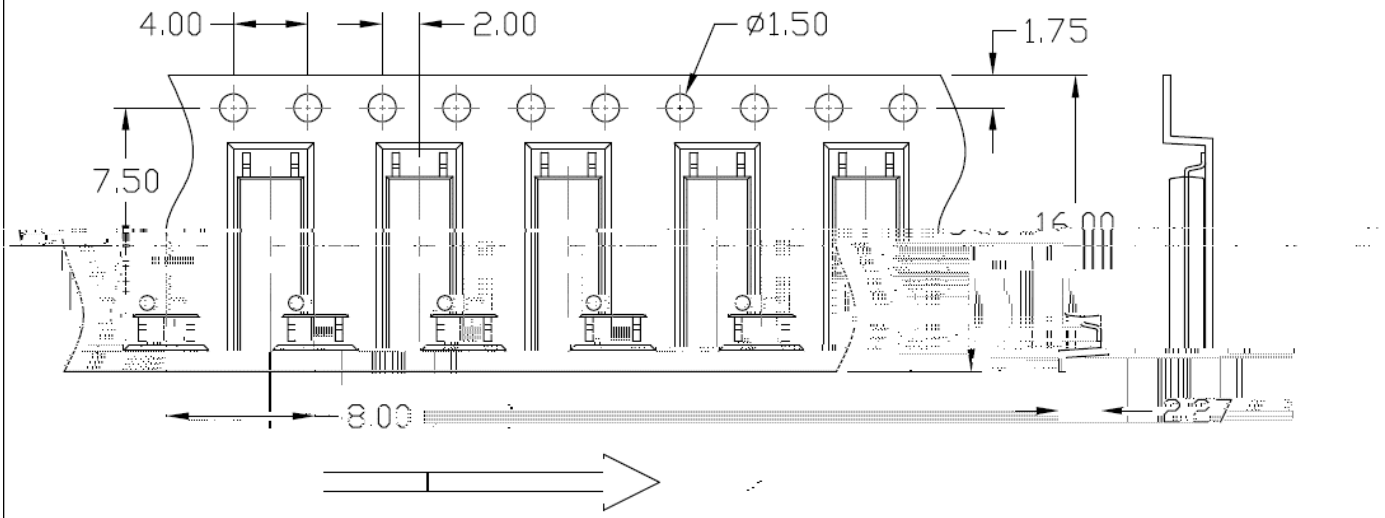


CA)) IE) TAPE SPECIFICATIONS (Dimenion in mm &nle$$ other / i$e $tated=

O%tion T1



O%tion T2



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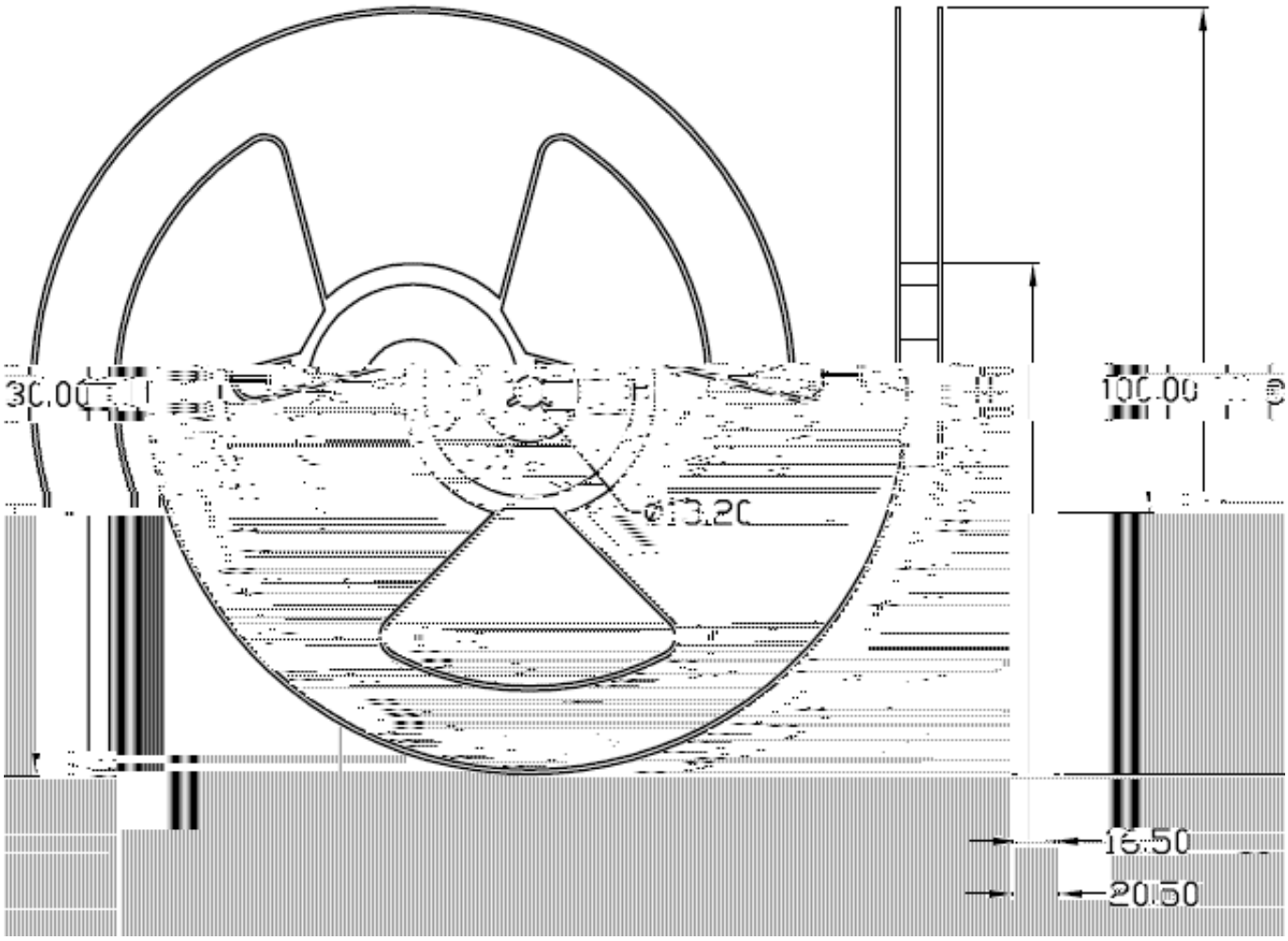
O%tion T1 > T2



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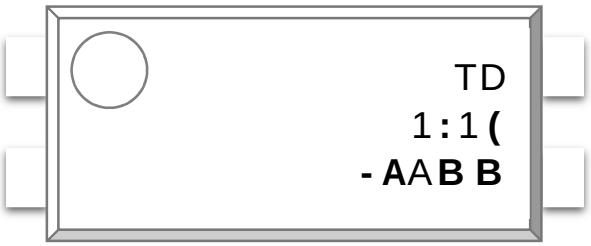

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BB @ Bor2 Bee2

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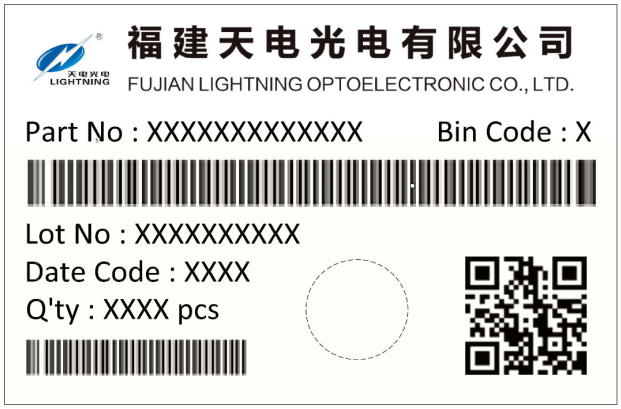

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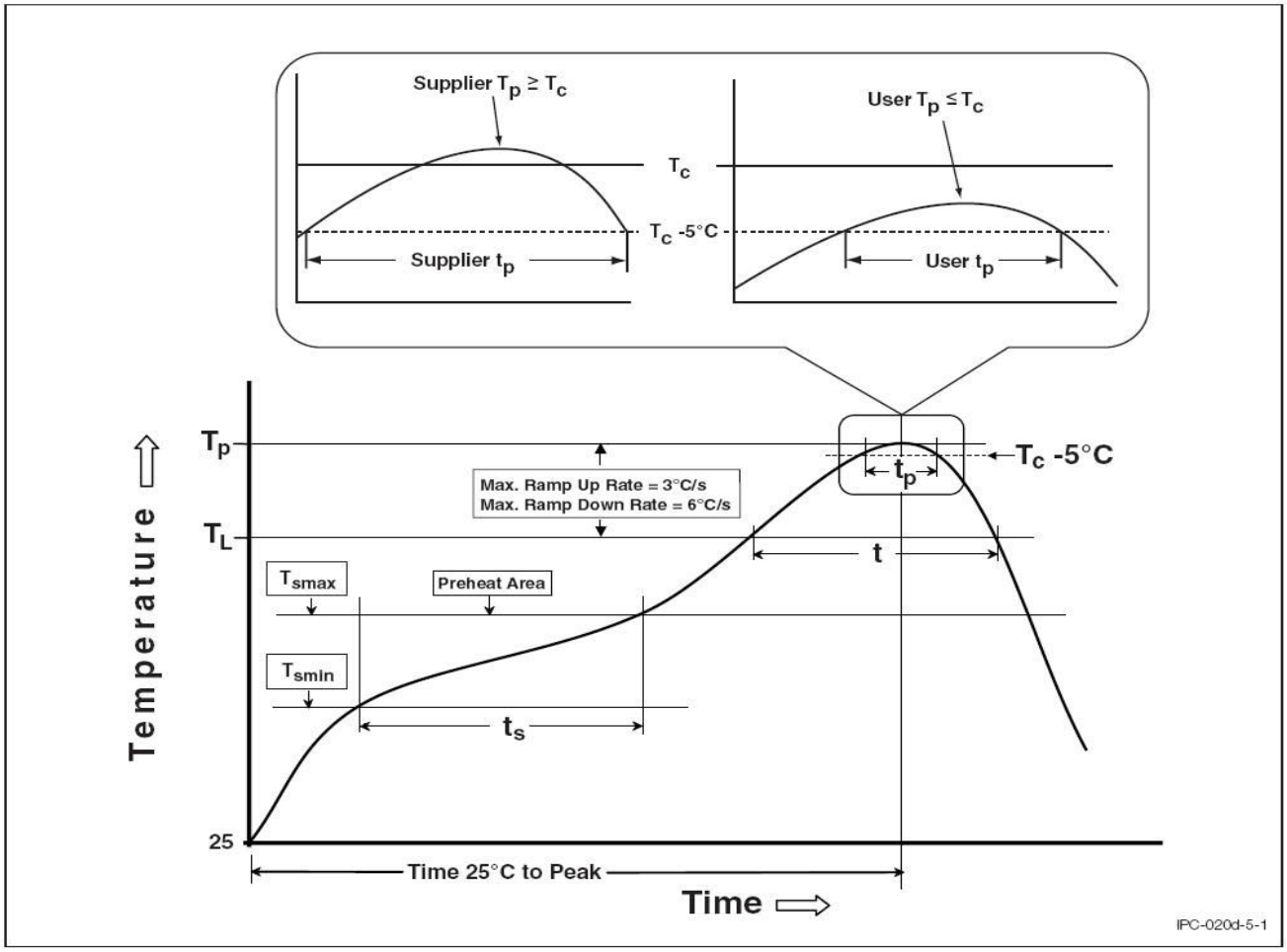

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